





Variable Gain Sub Femto Ampere Current Amplifier

Specifications	Test conditions	load impe	5 V, T _A = 25 edance = 1 20 minute	MΩ			ed)
Gain	Transimpedance Gain accuracy Gain drift	1 x 10 ⁴ 1 x 10 ¹³ V/A (load \ge 100 kΩ) ±1 % see table below					
Frequency Response	Lower cut-off frequency Upper cut-off frequency Adjustable low pass filter	00 Hz (see table below) ble to 3 settings (full bandwidth, 0.7 Hz and 0.1 Hz)					
		Upper cut-offRise timeFull BW (see table below)Fast (see table below)0.7 Hz0.5 s0.1 Hz5 s					
		Setting th for high r 0.7 Hz or	ing the low pass filter to full bandwidth is recommended high measurement speed. By setting the low pass filter to Hz or 0.1 Hz the peak-to-peak noise performance can mproved but the signal settling time will be longer.				
Input	Equ. input noise current Input bias current Input bias current drift Max. input current (full scale) Input offset compensation	minimum 10 ¹² or 1 20 fA typ factor 2 / see table	gain setting dependent, see table below minimum input noise is 0.4 fA peak-peak (at gain setting 10^{12} or 10^{13} V/A with low pass filter switched to 0.1 Hz) 20 fA typ. / 30 fA max. factor 2 / 10 °C see table below (value for linear amplification) adjustable by offset potentiometer, ±100 fA				
Performance Depending on Gain Setting	Gain setting (V/A)		10 ⁴	10 ⁵	10 ⁶	10 ⁷	10 ⁸
	Upper cut-off frequency (–3 dB)* Rise/fall time (10 % - 90 %)* Integrated input noise current (per Spectral input noise current dens Measured at Gain drift (/°C) Max. input current (± full scale) DC input impedance (// 5 pF)	eak-peak)*	400 Hz 0.8 ms 7 nA 45 pA 10 Hz 0.01 % 1 mA < 1 Ω	400 Hz 0.8 ms 7 nA 45 pA 10 Hz 0.01 % 0.1 mA < 1 Ω	400 Hz 0.8 ms 70 pA 0.45 pA 10 Hz 0.01 % 10 μA < 1 Ω	400 Hz 0.8 ms 70 pA 0.45 pA 10 Hz 0.01 % 1 μA < 1 Ω	150 Hz 2.3 ms 1.2 pA 15 fA 10 Hz 0.01 % 0.1 μA < 100 S
	Gain setting (continued) (V/A)		10 ⁹	10 ¹⁰	10 ¹¹	10 ¹²	10 ¹³
	Upper cut-off frequency (-3 dB)* Rise/fall time (10 % - 90 %)* Integrated input noise current (per Spectral input noise current dens Measured at Gain drift (/°C) Max. input current (± full scale) DC input impedance (// 5 pF)	eak-peak)* iity (/√Hz)			20 Hz 17 ms 50 fA 1.3 fA 1 Hz 0.03 % 0.1 nA < 10 kΩ		
	* The values for upper cut-off fre the table above are achieved with time). Lower peak-to-peak noise 0.1 Hz. In that case the bandwidt	n the low pa values can	ass filter se be achieve	et to "Full B ed by settir	W / Fast"	(full bandw pass filter f	idth/fast i to 0.7 Hz

Variable Gain Sub Femto Ampere Current Amplifier

pecifications (continued)		
Output	Output voltage Output impedance Max. output current	\pm 10 V (load ≥ 100 kΩ) 50 Ω (terminate with ≥ 100 kΩ load for best performance) ±30 mA
Adjustable Bias Voltage	General	An adjustable bias voltage is provided for directly biasing the device under test DUT (e.g. photodiode, high resistance semiconductor component). The bias voltage is connected to the inner conductor of the BNC input socket; the BNC-shield is always connected to analog ground. The bias voltage can be set either locally at the amplifier or through the remote interface. For measurements not requiring a bias voltage it car be fully disabled.
	Bias voltage range Bias current	± 10 V at inner conductor of BNC input socket max. ± 10 mA
Local Bias Adjustment	Bias switch setting Bias adjustment	set bias switch to position "Int." adjust bias voltage by bias potentiometer
Remote Bias Adjustment	Bias switch setting Bias adjustment Input impedance of control pin 8 Bias control voltage range Bias control polarity Example:	set bias switch to position "Ext." adjust bias by analog control voltage fed to pin 8 of Sub-D connector (referred to AGND pin 3) 200 k Ω ±10 V at pin 8 (referred to AGND pin 3) inverting feeding a control voltage of +2 V to pin 8 of the Sub-D connector leads to -2 V bias voltage at the inner conductor of the BNC input socket referred to BNC shield (analog ground, AGND)
Bias Deactivation	Bias switch setting	set bias switch to position "Off"
Bias Monitor Output	Range Connector Output impedance	\pm 10 V, shows the adjusted bias voltage at the BNC input (inner conductor referred to AGND pin 3) pin 7 of Sub-D connector (referred to AGND pin 3) 50 Ω (terminate with \geq 100 k Ω load for best performance)
Overload Indication	LED Overload output	lights when overload is detected non active: $<0.4 V @ 0 \dots -1 mA$, active: typ. 5 \dots 5.1 V @ 0 \dots 2 mA
Digital Control	Control input voltage range Control input current	LOW bit: -0.8+1.2 V, HIGH bit: +2.3 +12 V 0 mA @ 0 V; 1.5 mA @ +5 V; 4.5 mA @ +12 V
Auxiliary Power Output	Voltage	± 12 VDC, stabilized, max. ± 20 mA (at Sub-D, may be used for supplying external devices up to ± 20 mA)
Power Supply	Supply voltage Supply current	± 15 V +70 mA / –15 mA typ. (depends on operating conditions, recommended power supply capability minimum ± 150 mA)
Case	Weight Material	320 g (0.74 lb.) AlMg4.5Mn, nickel-plated
Temperature Range	Storage Temperature Operating Temperature	−40 +85 °C 0 +50 °C

SOPHISTICATED TOOLS FOR SIGNAL RECOVERY

0

F E M T

Variable Gain Sub Femto Ampere Current Amplifier

Absolute Maximum Ratings	Signal input voltage Electrostatic discharge Digital control input voltage Bias control input voltage Power supply voltage	±15 V relative to bias ±2 kV human body model (HBM) –5 V / +16 V ±12 V ±20 V				
Connectors	Input Output Bias voltage output Power supply	BNC, isolated, jack (female) BNC, jack (female) center pin of BNC input socket Lemo® series 1S, 3-pin fixed socket (mating plug type: FFA.1S.303.CLAC52) Pin 1: +15V Pin 2: -15V Pin 3: GND PIN 2 -Vs PIN 3 GND				
	Control Port	Sub-D 25-pin, female, qual. class 2 Pin 1: +12V (stabilized power supply output) Pin 2: -12V (stabilized power supply output) Pin 3: AGND (analog ground) Pin 4: NC Pin 5: overload output (referred to AGND pin 3) Pin 6: signal output (connected to BNC output connector) Pin 7: bias voltage monitor output (referred to AGND pin 3) Pin 8: bias control voltage input (referred to AGND pin 3) Pin 9: DGND (ground for digital control pins 10 - 13) Pin 10: digital control input: gain, LSB Pin 11: digital control input: gain Pin 12: digital control input: gain Pin 13: digital control input: gain, MSB Pin 14 - 25: NC				
OPHISTICATED 1	TOOLS FOR SIGNAL	RECOVERY FENTO				

Variable Gain Sub Femto Ampere Current Amplifier

Remote Control Operation	General	Remote control input bits are opto-isolated. For remote control operation set the rotary gain switch to the "Remote" position and select the desired gain setting via a bit code at the digital inputs. Switch settings "0.1 Hz / Full BW / 0.7 Hz" and "Bias Ext. / Off / Int." are not remote controllable.					
	Gain Setting	Gain (V/A)	Pin 13 MSB	Pin 12	Pin 11	Pin 10 LSB	
		10 ⁴	LOW	LOW	LOW	LOW	_
		10 ⁵	LOW	LOW	LOW	HIGH	
		10 ⁶	LOW	LOW	HIGH	LOW	
		10 ⁷	LOW	LOW	HIGH	HIGH	
		10 ⁸	LOW	HIGH	LOW	LOW	
		10 ⁹	LOW	HIGH	LOW	HIGH	
		10 ¹⁰	LOW	HIGH	HIGH	LOW	
		10 ¹¹	LOW	HIGH	HIGH	HIGH	
		10 ¹²	HIGH	LOW	LOW	LOW	
		10 ¹³	HIGH	LOW	LOW	HIGH	
OPHISTICATED 1				F			











Datasheet DDPCA-300 Variable Gain **Sub Femto Ampere Current Amplifier** Dimensions 157 150 137 5 OUT NG 0 15 4 xt. # 0_0 Ċ DDPCA-300 **F E 🏷 T O** \square ø 3.2 2 0 $\langle \circ \rangle$ 0 44.2 28.5 15.4 all measures in mm unless otherwise noted DZ-DDPCA-300 R6 FEMTO Messtechnik GmbH Specifications are subject to change without notice. Information provided herein is believed to be accurate and Klosterstr. 64 reliable. However, no responsibility is assumed by FEMTO Messtechnik GmbH for its use, nor for any infringement 10179 Berlin · Germany of patents or other rights of third parties which may result from its use. No license is granted by implication or Phone: +49 30 280 4711-0 otherwise under any patent or patent rights of FEMTO Messtechnik GmbH. Product names mentioned may also be Fax: +49 30 280 4711-11 trademarks used here for identification purposes only. Email: info@femto.de © by FEMTO Messtechnik GmbH · Printed in Germany www.femto.de

0

П

П